



VERIFICATION OF A TRANSLATION

I, Tetsuo AKIYOSHI, of 5th Floor, Shintoshicenter Bldg., 24-1, Tsurumaki 1-chome, Tama-shi, Tokyo 206-0034 Japan, declare that I am well acquainted with both the Japanese and English languages, and that the attached is an accurate translation, to the best of my knowledge and ability, of the Japanese language Patent Application No. JP 2003-083500 filed on March 25, 2003.

Signature

A handwritten signature in black ink, appearing to read "Tetsuo Akiyoshi", written over a horizontal line.

Date

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[TITLE OF THE INVENTION] CODING APPARATUS AND CODING METHOD

[SCOPE OF CLAIMS FOR PATENT]

[Claim 1] A coding apparatus comprising:

a coder which encodes transmission data and outputs systematic bits and parity bits;

a modulator that modulates the output systematic bits and parity bits in mutually different modulation schemes; and

a transmitter that transmits the modulated systematic bits and parity bits from respective antennas.

[Claim 2] The coding apparatus according to claim 1, wherein the modulator has:

a modulation scheme instructor that applies different modulation schemes to the systematic bits and parity bits;

a bit arrangement determiner that determines bit arrangements of the systematic bits and the parity bits according to the applied modulation schemes; and

a mapping section that performs symbol mapping on the systematic bits and the parity bits with the determined bit arrangements.

[Claim 3] The coding apparatus according to claim 2,

wherein the modulation scheme instructor applies a modulation scheme with a larger modulation M-ary number to the parity bits than in the systematic bits, and

wherein the bit arrangement determiner determines a bit arrangement where the parity bits are arranged on the in-phase axis and the quadrature axis.

[Claim 4] The coding apparatus according to claim 2,

wherein the coder outputs a plurality of parity bits for one systematic bit, and

wherein the bit arrangement determiner determines different bit arrangements for the plurality of parity bits.

[Claim 5] The coding apparatus according to claim 2,

wherein the modulator further has an arrangement axis exchanger that exchanges arrangement axes on coordinates to perform bit arrangements determined in the bit arrangement determiner, and

wherein the mapping section performs symbol mapping on the systematic bits and the parity bits with bit arrangements on the coordinates where the arrangement axes are exchanged.

[Claim 6] The coding apparatus according to claim 2, further comprising a detector that detects the number of retransmission times of transmission data, and

wherein the bit arrangement determiner changes bit arrangements of the systematic bits and the parity bits corresponding to the detected number of retransmission times.

[Claim 7] A coding method comprising the steps of:  
encoding transmission data to output systematic bits and parity bits;

modulating the output systematic bits and parity bits in mutually different modulation schemes; and

transmitting the modulated systematic bits and parity bits from respective antennas.

[DETAILED DESCRIPTION OF THE INVENTION]

[0001]

[Technical Field of the Invention]

The present invention relates to a coding apparatus and coding method, and more particularly, to a coding apparatus and coding method for performing space-time turbo coding on transmission data.

[0002]

[Prior Art]

In recent years, in error correcting codes in

wireless communications, attention has been focused on turbo codes nearing the Shannon limit that is theoretical limit of transmission speed enabling transmission without errors. In turbo coding, generally, a plurality of bit sequences is output such as a sequence of systematic bits that are information bits, a sequence of parity bits that are redundant bits obtained by performing convolutional coding on the systematic bits, and another sequence of parity bits obtained by performing interleaving on the systematic bits and then convolutional coding thereon.

[0003]

A technique for transmitting thus output plural bit sequences from respective plural transmission antennas is called space-time turbo coding. Space-time turbo coding is one of SDM (Space Division Multiplexing) that spatially multiplexes a plurality of signals on the transmission side.

[0004]

In space-time turbo coding, a plurality of bit sequences generated by turbo coding is modulated, subjected to symbol mapping, and transmitted from a plurality of transmission antennas assigned to respective bit sequences (for example, see non-patent document 1 and non-patent document 2).

[0005]

By this means, it is possible to achieve both the diversity gain due to transmission using a plurality of transmission antennas and significant coding gain by turbo codes.

[0006]

[Non-patent document 1]

IEEE TRANSACTIONS ON COMMUNICATIONS, VOL. 49, NO.

1, JANUARY 2001 "Space-Time Turbo Codes with Full Antenna Diversity"

[Non-patent document 2]

IEEE JOURNAL ON SELECTED AREAS IN COMMUNICATIONS,  
VOL. 19, NO. 5, MAY 2001 "Turbo-Coded Modulation for  
Systems with Transmit and Receive Antenna Diversity over  
Block Fading Channels: System Model, Decoding Approaches,  
and Practical Considerations"

[0007]

[Problems to be Solved by the Invention]

However, in performing space-time turbo coding,  
using M-ary modulation to further improve the  
transmission efficiency causes such a problem that a  
large number of overlaps of signal points (hereinafter,  
referred to as "degeneracy") occur in symbol mapping.

[0008]

This problem will specifically be described below  
with reference to FIGS.8. FIGS.8 are diagrams showing  
signal point constellations when a plurality of bit  
sequences obtained by turbo coding is transmitted from  
two transmission antennas, as an example of space-time  
turbo coding.

[0009]

FIG.8A is a diagram showing one example of signal  
point constellations when BPSK (Binary Phase Shift  
Keying) modulation is performed on each of the bit  
sequences transmitted from two transmission antennas.

[0010]

When each of BPSK-modulated bit sequences is  
subjected to symbol mapping and multiplexed, originally  
four (=2x2) signal point candidates should exist.  
However, in the case as shown in FIG.8A, a white point  
shown in the figure represents overlapping two points  
among four points, and degeneracy occurs.

[0011]

Similarly, FIG.8B is a diagram showing one example of signal point constellations when QPSK (Quadrature Phase Shift Keying) modulation is performed on each of the bit sequences transmitted from two transmission antennas.

[0012]

When each of QPSK-modulated bit sequences is subjected to symbol mapping and multiplexed, originally sixteen ( $=4 \times 4$ ) signal point candidates should exist. However, in the case as shown in FIG.8B, points except four black points shown in the figure represent overlapping two or four points, and degeneracy occurs. In other words, in the case of QPSK modulation, degeneracy occurs in 75% of entire points, and the probability of occurrence of degeneracy is higher than that in BPSK modulation.

[0013]

Further, FIG.8C is a diagram showing one example of signal point constellations when 16QAM (16 Quadrature Amplitude Modulation) is performed on each of the bit sequences transmitted from two transmission antennas.

[0014]

When each of 16QAM-modulated bit sequences is subjected to symbol mapping and multiplexed, originally 256 ( $=16 \times 16$ ) signal point candidates should exist. However, in the case as shown in FIG.8C, points except four black points shown in the figure represent overlapping two or four points, and degeneracy occurs. In other words, in 16QAM, degeneracy occurs in 98% of entire points, and the probability of occurrence of degeneracy is higher than that in QPSK modulation.

[0015]

Thus, in performing M-ary modulation in space-time

turbo coding, since a large amount of degeneracy occurs, demodulation performance deteriorates in a reception apparatus. In order to prevent the demodulation performance from deteriorating, it is necessary to increase transmission power in a transmission apparatus to enhance reception quality in the reception apparatus. As a result, however, increased interfering power is provided to reception apparatuses other than the transmission destination, and adverse effects are exerted on the entire wireless communication system.

[0016]

The present invention has been made with consideration given to the aforementioned circumstances, and an object of the present invention is to provide a coding apparatus and coding method capable of preventing deterioration of demodulation performance without increasing interfering power.

[0017]

[Means for Solving the Problems]

A coding apparatus of the present invention adopts a configuration including a coder which encodes transmission data and outputs systematic bits and parity bits; a modulator that modulates the output systematic bits and parity bits in mutually different modulation schemes; and a transmitter that transmits the modulated systematic bits and parity bits from respective antennas.

[0018]

According to this configuration, obtained coded systematic bits and parity bits are modulated in mutually different modulation schemes, and transmitted from corresponding antennas. Therefore, even when the transmitted systematic bits and parity bits are multiplexed, the probability of occurrence of degeneracy is low, and it is possible to prevent degradation in demodulation performance without increasing interfering power.



[0019]

The coding apparatus of the present invention adopts a configuration wherein the modulator has a modulation scheme instructor that applies different modulation schemes to the systematic bits and parity bits; a bit arrangement determiner that determines bit arrangements of the systematic bits and the parity bits according to the applied modulation schemes; and a mapping section that performs symbol mapping on the systematic bits and the parity bits with the determined bit arrangements.

[0020]

According to this configuration, different modulation schemes are applied to the systematic bits and parity bits, and bit arrangements are determined to perform symbol mapping. Therefore, the demodulation schemes are different between the systematic bits and parity bits, and for example, in applying M-ary modulation in space-time turbo coding, it is possible to decrease the probability of occurrence of degeneracy.

[0021]

The coding apparatus of the present invention adopts a configuration wherein the modulation scheme instructor applies a modulation scheme with a larger modulation M-ary number to the parity bits than in the systematic bits, and the bit arrangement determiner determines a bit arrangement where the parity bits are arranged on the in-phase axis and the quadrature axis.

[0022]

According to this configuration, a modulation scheme with a larger modulation M-ary number is applied to the parity bits than in the systematic bits, the parity bits are arranged on the in-phase axis and quadrature axis, and it is thereby possible to reduce interference between two parity bits occurring in turbo coding, for example.

[0023]

The coding apparatus of the present invention

adopts a configuration wherein the coder outputs a plurality of parity bits for one systematic bit, and the bit arrangement determiner determines different bit arrangements for the plurality of parity bits.

[0024]

According to this constitution, since a plurality of parity bits are output for one systematic bit, and different bit arrangements are determined for the plurality of parity bits, it is possible to prevent reception quality in only one type of parity bits from always deteriorating in the reception apparatus, for example, between two types of parity bits occurring in turbo coding, and uniform reception quality.

[0025]

The coding apparatus of the present invention adopts a configuration wherein the modulator further has an arrangement axis exchanger that exchanges arrangement axes on coordinates to perform bit arrangements determined in the bit arrangement determiner, and the mapping section performs symbol mapping on the systematic bits and the parity bits with bit arrangements on the coordinates where the arrangement axes are exchanged.

[0026]

According to this configuration, since arrangement axes in the bit arrangement are exchanged and then symbol mapping is performed on the systematic bits and parity bits, it is possible to provide the bit arrangement with variations by exchanging the arrangement axes, and prevent the same error from always occurring on the propagation path.

[0027]

The coding apparatus of the present invention adopts a configuration further including a detector that detects the number of retransmission times of transmission data wherein the bit arrangement determiner changes bit arrangements of the systematic bits and the parity bits corresponding to the detected number of

retransmission times.

[0028]

According to this constitution, since bit arrangements of the systematic bits and parity bits are changed corresponding to the number of retransmission times of transmission data, it is possible to prevent the same error from occurring in the retransmitted data, and improve the throughput of the entire system.

[0029]

A coding method of the present invention includes the steps of encoding transmission data to output systematic bits and parity bits, modulating the output systematic bits and parity bits in mutually different modulation schemes and transmitting the modulated systematic bits and parity bits from respective antennas.

[0030]

According to this method, obtained coded systematic bits and parity bits are modulated in mutually different modulation schemes, and transmitted from corresponding antennas. Therefore, even when the transmitted systematic bits and parity bits are multiplexed, the probability of occurrence of degeneracy is low, and it is possible to prevent degradation in demodulation performance without increasing interfering power.

[0031]

[Embodiments of the Invention]

A gist of the invention is modulating bit sequences in mutually different modulation schemes in transmitting systematic bits and parity bits obtained by coding from a plurality of transmission antennas.

[0032]

Embodiments of the present invention will specifically be described below with reference to accompanying drawings.

[0033]

(Embodiment 1)

FIG.1 is a block diagram showing a principal

configuration of a space-time turbo coding apparatus according to Embodiment 1 of the invention. The space-time turbo coding apparatus as shown in FIG.1 is mainly comprised of turbo coding section 100, selecting section 200, mapping sections 300-1 and 300-2, radio transmission sections 400-1 and 400-2, and modulation scheme instructing section 500. Moreover, the turbo coding section 100 has interleaver 110, coding sections 120-1 and 120-2, puncturing sections 130-1 and 130-2, and deinterleaver 140.

[0034]

Turbo coding section 100 performs turbo coding on transmission data, and outputs systematic bits that are information bits of the transmission data, and parity bits that are redundant bits obtained by performing convolutional coding on the systematic bits.

[0035]

More specifically, turbo coding section 100 outputs the transmission data as a sequence of the systematic bits to mapping section 300-1, while further outputting two sequences of parity bits to selecting section 200 as described below.

[0036]

In other words, coding section 120-1 performs convolutional coding on the transmission data. Puncturing section 130-1 performs puncturing on the coded bit sequence obtained by convolutional coding in coding section 120-1 to thin bits. An output of puncturing section 130-1 is output to selecting section 200 as one parity bit sequence (hereinafter, referred to as "parity bit 1").

[0037]

Moreover, interleaver 110 rearranges (interleaves) the order of the bits of the transmission data. Coding

section 120-2 performs convolutional coding on the bit sequence interleaved by interleaver 110. Puncturing section 130-2 performs puncturing on the coded bit sequence obtained by convolutional coding in coding section 120-2 to thin bits. With respect to the bit sequence output from puncturing section 130-2, deinterleaver 140 restores (deinterleaves) the order of bits rearranged by interleaver 110 to the original order. An output of deinterleaver 140 is output to selecting section 200 as another parity bit sequence (hereinafter, referred to as "parity bit 2").

[0038]

In thinning bits from the coded bit sequence subjected to convolutional coding, each of puncturing sections 130-1 and 130-2 thins bits based on a modulation scheme notified from modulation scheme instructing section 500, which will be described later.

[0039]

Furthermore, since deinterleaver 140 restores the order of bits rearranged by interleaver 110, each of two parity bit sequences concurrently output from turbo coding section 100 corresponds to the concurrently output systematic bits.

[0040]

Selecting section 200 selects either of the two parity bit sequences output from turbo coding section 100 to output to mapping section 300-2.

[0041]

Mapping section 300-1 modulates the systematic bits output from turbo coding section 100 to perform symbol mapping. At this time, mapping section 300-1 modulates the systematic bits in a modulation scheme instructed from modulation scheme instructing section 500.

[0042]

Radio transmission section 400-1 performs predetermined radio transmission processing (such as D/A

conversion and upconverting) on the systematic bits and transmits a radio signal via an antenna.

[0043]

Mapping section 300-2 modulates the parity bits output from selecting section 200 to perform symbol mapping. At this time, mapping section 300-2 modulates the parity bits in a modulation scheme instructed from modulation scheme instructing section 500. The modulation scheme instructed to mapping section 300-2 is different from the modulation scheme instructed to mapping section 300-1. In other words, the systematic bits and parity bits are modulated in different modulation schemes.

[0044]

Radio transmission section 400-2 performs the predetermined radio transmission processing (such as D/A conversion and upconverting) on the parity bits and transmits a radio signal via an antenna.

[0045]

Modulation scheme instructing section 500 instructs modulation schemes to puncturing sections 130-1 and 130-2 and mapping sections 300-1 and 300-2. At this time, modulation scheme instructing section 500 instructs a modulation scheme to modulate the systematic bits to mapping section 300-1, while instructing another modulation scheme different from such a modulation scheme to mapping section 300-2 as a modulation scheme to modulate the parity bits. Further, modulation scheme instructing section 500 notifies the modulation schemes for systematic bits and parity bits to puncturing sections 130-1 and 130-2, respectively.

[0046]

Next, the operation of the space-time turbo coding

apparatus configured as described above will be described below.

[0047]

First, turbo coding section 100 performs turbo coding on transmission data. In other words, systematic bits from the transmission data are output to mapping section 300-1. At the same time, the transmission data undergoes convolutional coding in coding section 120-1, and then, puncturing in puncturing section 130-1, and parity bit 1 is output to selecting section 200. Further, the transmission data undergoes interleaving in interleaver 110, convolutional coding in coding section 120-2, puncturing in puncturing section 130-2, and deinterleaving in deinterleaver 140, and parity bit 2 is output to selecting section 200.

[0048]

Puncturing in puncturing sections 130-1 and 130-2 is performed based on modulation schemes for the systematic bits and parity bits notified from modulation scheme instructing section 500. In other words, since modulation schemes are different between the systematic bits and parity bits, the number of bits to transmit per symbol is different, and therefore, in order not to degrade the correspondence relationship between the systematic bits and parity bits, bits are thinned from the coded bit sequence.

[0049]

For example, when the systematic bits are modulated in BPSK and the parity bits are modulated in QPSK, one bit is transmitted in one symbol in the systematic bits, while two bits are transmitted in one symbol in the parity bits. Accordingly, the correspondence relationship

between the systematic bits and parity bits does not deteriorate, without increasing the number of bits thinned from the coded bit sequence in puncturing sections 130-1 and 130-2.

[0050]

Meanwhile, when the systematic bits are modulated in QPSK and the parity bits are modulated in BPSK, two bits are transmitted in one symbol in the systematic bits, while one bit is transmitted in one symbol in the parity bits. Accordingly, puncturing sections 130-1 and 130-2 need to thin a larger number of bits than general from the coded bit sequence.

[0051]

Thus, puncturing is performed to accommodate the difference in modulation scheme between the systematic bits and parity bits, and each of parity bits 1 and 2 is output to selecting section 200.

[0052]

Then, selecting section 200 selects either parity bit 1 or 2 to output to mapping section 300-2. In selecting parity bits, selecting section 200 may alternately select parity bits 1 and 2, or select according to a predetermined rule. In the case of selecting either parity bit 1 or 2, the section 200 outputs to mapping section 300-2 parity bits corresponding to the systematic bits output to mapping section 300-1.

[0053]

Then, mapping section 300-1 modulates the systematic bits in the modulation scheme instructed from modulation scheme instructing section 500 to perform symbol mapping. Meanwhile, mapping section 300-2 modulates the parity bits in the modulation scheme



instructed from modulation scheme instructing section 500 to perform symbol mapping.

[0054]

Modulation scheme instructing section 500 instructs different modulation schemes as modulation schemes to apply to the systematic bits and parity bits, and therefore, mapping sections 300-1 and 300-2 perform modulation in different modulation schemes.

[0055]

For example, modulation scheme instructing section 500 instructs mapping section 300-1 to perform BPSK modulation, while instructing mapping section 300-2 to perform QPSK modulation. In this case, the transmission efficiency is higher in the parity bits than in the systematic bits, thereby increasing the error coding gain due to turbo coding, and the accuracy in demodulated data becomes high in the reception apparatus.

[0056]

Further, for example, modulation scheme instructing section 500 instructs mapping section 300-1 to perform QPSK modulation, while instructing mapping section 300-2 to perform BPSK modulation. In this case, the transmission efficiency is high in the systematic bits, thereby increasing the transmission rate of information.

[0057]

Furthermore, in either of above-mentioned cases, as described later, degeneracy is harder to occur in the reception apparatus than in the case of performing QPSK modulation on both the systematic bits and parity bits.

[0058]

Thus modulated and symbol-mapped systematic bits and parity bits are subjected to the predetermined radio transmission processing (such as D/A conversion and

upconverting) in radio transmission sections 400-1 and 400-2, and transmitted from antennas 450-1 and 450-2, respectively. Respective signals transmitted from antennas 450-1 and 450-2 are spatially multiplexed and received in the reception apparatus.

[0059]

FIGS.2 are diagrams showing examples of systematic bits and parity bits modulated in mutually different modulation schemes being spatially multiplexed. In addition, FIGS.2 show the examples such that the largest amount of degeneracy occurs in the reception apparatus.

[0060]

For example, when either of systematic bits or parity bits are modulated in QPSK and the other bits are modulated in BPSK, eight ( $=4 \times 2$ ) signal point candidates should exist originally. According to the space-time turbo coding apparatus of this Embodiment, as shown in FIG. 2A, in only a white point in the figure, two points among eight points overlap and degeneracy occurs, and the probability of occurrence of degeneracy is 25%. This probability is much smaller than 75% in the case of performing QPSK modulation on both the systematic bits and parity bits.

[0061]

Further, for example, when either of systematic bits or parity bits are modulated in 16QAM and the other bits are modulated in QPSK, 64 ( $=16 \times 4$ ) signal point candidates should exist originally. According to the space-time turbo coding apparatus of this Embodiment, as shown in FIG.2B, degeneracy occurs in points except sixteen black points in the figure, and the probability of occurrence of degeneracy is 75%. Also in this case,

the probability is smaller than 98% in the case of performing 16QAM on both the systematic bits and parity bits.

[0062]

Thus, according to this Embodiment, puncturing of parity bits is performed to accommodate the difference between mutually different modulation schemes for use in the systematic bits and parity bits, and the systematic bits and parity bits are modulated in mutually different modulation schemes to undergo symbol mapping, and transmitted from respective different antennas. Thus it is possible to achieve both the diversity gain and coding gain due to space-time turbo coding, suppress the probability of occurrence of degeneracy in the reception apparatus, and prevent degradation in demodulation performance without increasing interfering power.

[0063]

(Embodiment 2)

A feature of Embodiment 2 of the present invention lies in mapping parity bits 1 and 2 on the in-phase axis and quadrature axis to decrease interference between parity bits.

[0064]

FIG.3 is a block diagram showing a principal configuration of a space-time turbo coding apparatus according to this Embodiment. In the space-time turbo coding apparatus as shown in the figure, the same sections as in the space-time turbo coding apparatus as shown in FIG.1 are assigned the same reference numerals to omit descriptions thereof. The space-time turbo coding apparatus as shown in FIG.3 is mainly comprised of turbo coding section 100, selecting section 200, mapping sections 300-1 and 300-2, radio transmission sections 400-1 and 400-2, modulation scheme instructing section 500 and bit arrangement determining section 600.

[0065]

Under the modulation scheme of parity bits instructed from modulation scheme instructing section 500, bit arrangement determining section 600 determines a bit arrangement so that two parity bits output from selecting section 200 are mapped on the in-phase axis and quadrature axis, and notifies the determined bit arrangement to mapping section 300-2.

[0066]

Next, the operation of the space-time turbo coding apparatus configured as described above will be described below.

[0067]

First, as in Embodiment 1, turbo coding section 100 performs turbo coding on transmission data. As a result of turbo coding, obtained systematic bits are output to mapping section 300-1, while parity bits are output to mapping section 300-2 from selecting section 200.

[0068]

Mapping section 300-1 modulates the systematic bits in the modulation scheme instructed from modulation scheme instructing section 500, and performs symbol mapping to be in the bit arrangement determined in bit arrangement determining section 600. Meanwhile, mapping section 300-2 modulates the parity bits in the modulation scheme instructed from modulation scheme instructing section 500, and performs symbol mapping to be in the bit arrangement determined in bit arrangement determining section 600.

[0069]

Also in this Embodiment, modulation scheme instructing section 500 instructs different modulation schemes as modulation schemes to apply to the systematic bits and parity bits, and therefore, mapping sections

300-1 and 300-2 perform modulation in different modulation schemes. In addition, it is assumed in this Embodiment that a modulation scheme providing higher transmission efficiency is applied to the parity bits.  
[0070]

Bit arrangement determining section 600 determines a bit arrangement such that the parity bits are mapped on the in-phase axis and quadrature axis, as shown in FIGS.4.  
[0071]

In other words, when modulation scheme instructing section 500 instructs bit arrangement determining section 600 to apply BPSK modulation to the systematic bits and QPSK modulation to the parity bits, as shown in FIG.4A, the section 600 determines a bit arrangement so that a first parity bit ( $P_1$ ) is arranged on the in-phase axis, and that a second parity bit ( $P_2$ ) is arranged on the quadrature axis.  
[0072]

Further, when modulation scheme instructing section 500 instructs bit arrangement determining section 600 to apply QPSK modulation to the systematic bits and 16QAM to the parity bits, as shown in FIG.4B, the section 600 determines a bit arrangement so that a first parity bit ( $P_{11}$ ) convolutional coded in coding section 120-1 and a first parity bit ( $P_{12}$ ) convolutional coded in coding section 120-2 are arranged on the in-phase axis, and that a second parity bit ( $P_{21}$ ) convolutional coded in coding section 120-1 and a second parity bit ( $P_{22}$ ) convolutional coded in coding section 120-2 are arranged on the quadrature axis.  
[0073]

By this means, it is possible to reduce interference between parity bits respectively convolutional coded in coding sections 120-1 and 120-2.  
[0074]

When bit arrangements are thus determined, the

determined bit arrangements are respectively notified to mapping sections 300-1 and 300-2. Then, mapping section 300-1 modulates the systematic bits and performs symbol mapping. Further, mapping section 300-2 modulates the parity bits and performs symbol mapping.

[0075]

Thus modulated and symbol-mapped systematic bits and parity bits are subjected to the predetermined radio transmission processing (such as D/A conversion and upconverting) in radio transmission sections 400-1 and 400-2, and transmitted from antennas 450-1 and 450-2, respectively. Respective signals transmitted from antennas 450-1 and 450-2 are spatially multiplexed and received in the reception apparatus.

[0076]

Even when a phase rotation due to fading or the like is added on the propagation path until the signal is received in the reception apparatus, the cases as shown in FIGS.2 are the worst cases where the largest amount of degeneracy occurs. Accordingly, the probability of occurrence of degeneracy is smaller than in the case of applying the same modulation scheme to the systematic bits and parity bits.

[0077]

Thus, according to this Embodiment, the systematic bits and parity bits are modulated in mutually different modulation schemes, while performing symbol mapping such that the parity bits are arranged on the in-phase axis and the quadrature axis, and transmitted from respective different antennas. Thus it is possible to suppress the probability of occurrence of degeneracy in the reception apparatus, prevent deterioration in demodulation

performance without increasing interfering power, and reduce interference between the parity bits.

[0078]

(Embodiment 3)

A feature of Embodiment 3 of the present invention lies in performing a bit arrangement so as to uniform reception quality in receiving parity bits 1 and 2 generated by turbo coding.

[0079]

The principal configuration of the space-time turbo coding apparatus according to this Embodiment is the same as in the space-time turbo coding apparatus (FIG.3) according to Embodiment 2, and descriptions thereof are omitted. In addition, in this Embodiment, the operation of bit arrangement determining section 600 is different from that in Embodiment 2.

[0080]

In this Embodiment, under the modulation scheme of parity bits instructed from modulation scheme instructing section 500, bit arrangement determining section 600 determines a bit arrangement so that two parity bits output from selecting section 200 are mapped on the in-phase axis and quadrature axis. Further, bit arrangement determining section 600 determines the bit arrangement so that an arrangement of parity bit 1 convolutional coded in coding section 120-1 is different from that of parity bit 2 convolutional coded in coding section 120-2.

[0081]

More specifically, when modulation scheme instructing section 500 instructs bit arrangement determining section 600 to apply QPSK modulation to the systematic bits and 16QAM to the parity bits, as shown

in FIG. 6, the section 600 arranges parity bits ( $P_{11}$  and  $P_{12}$ ) convolutional coded in coding section 120-1 on the in-phase axis as in Embodiment 2. Meanwhile, with respect to parity bits ( $P_{21}$  and  $P_{22}$ ) convolutional coded in coding section 120-2, the section 600 changes the order of bits and arranges the bits on the quadrature axis.

[0082]

In Embodiment 2 (FIGS.4), with respect to parity bits ( $P_{11}$  and  $P_{21}$ ) convolutional coded in coding section 120-1, bits taking different values are always arranged on different quadrants on coordinates comprised of arrangement axes. Meanwhile, with respect to parity bits ( $P_{12}$  and  $P_{22}$ ) convolutional coded in coding section 120-2, bits taking different values are sometimes arranged on the same quadrant on coordinates. Accordingly, reception quality of parity bits  $P_{11}$  and  $P_{21}$  is relatively high in the reception apparatus, while the reception quality of parity bits  $P_{12}$  and  $P_{22}$  are always low in the reception apparatus.

[0083]

In this Embodiment, since parity bits  $P_{21}$  and  $P_{22}$  are exchanged in the bit arrangement, reception quality of parity bit  $P_{22}$  convolutional coded in coding section 120-2 becomes relatively high, and irrespective of whether bits are convolutional coded in coding section 120-1 or 120-2, the reception quality of parity bits are made uniform.

[0084]

Thus, according to this Embodiment, the systematic bits and parity bits are modulated in mutually different modulation schemes, while in performing symbol mapping such that the parity bits are arranged on the in-phase axis and the quadrature axis, part of bits are exchanged in the bit arrangement between parity bit 1 and parity



bit 2, and the systematic bits and parity bits are transmitted from respective different antennas. Thus it is possible to suppress the probability of occurrence of degeneracy in the reception apparatus, prevent degradation in demodulation performance without increasing interfering power, and reduce interference between the parity bits, while the reception quality is made uniform among a plurality of parity bit sequences.

[0085]

(Embodiment 4)

A feature of Embodiment 4 of the present invention lies in changing bit arrangements of the systematic bits and parity bits corresponding to the number of retransmission times.

[0086]

FIG.6 is a block diagram showing a principal configuration of a space-time turbo coding apparatus according to this Embodiment. In the space-time turbo coding apparatus as shown in the figure, the same sections as in the space-time turbo coding apparatus as shown in FIG.1 or FIG.3 are assigned the same reference numerals to omit descriptions thereof. The space-time turbo coding apparatus as shown in FIG.6 is mainly comprised of turbo coding section 100, selecting section 200, mapping sections 300-1 and 300-2, radio transmission sections 400-1 and 400-2, modulation scheme instructing section 500, bit arrangement determining section 600, arrangement axis exchanging section 700, and retransmission control section 800.

[0087]

According to control from retransmission control section 800, bit arrangement determining section 600 determines a bit arrangement of parity bits as in

Embodiment 2 or 3. In other words, bit arrangement determining section 600 determines a bit arrangement such that the parity bits are mapped on the in-phase axis and quadrature axis, or another bit arrangement such that the parity bits are mapped on the in-phase axis and quadrature axis, while parity bit 1 and parity bit 2 are different in arrangement.

[0088]

According to control from retransmission control section 800, arrangement axis exchanging section 700 exchanges the arrangement axis in the bit arrangement determined in bit arrangement determining section 600.

[0089]

Retransmission control section 800 controls bit arrangement determining section 600 and arrangement axis exchanging section 700 corresponding to number-of-retransmission-time information indicative of the number of times the reception apparatus makes a retransmission request for the same data. More specifically, retransmission control section 800 determines whether or not to operate bit arrangement determining section 600 and arrangement axis exchanging section 700 so that bit arrangements of the systematic bits and parity bits are made different every retransmission time.

[0090]

Generally, when retransmission of the same data is required, the data received in the reception apparatus includes a large amount of errors due to effects of fading and the like on the propagation path, and does not meet the predetermined quality. Accordingly, for example, when the fading variation is slow, if a signal of the same bit arrangement is transmitted at the time of

retransmission, the possibility is considered high that the retransmitted data is similarly erroneous.

[0091]

Then, in this Embodiment, a signal is transmitted using a different bit arrangement every retransmission time.

[0092]

Next, the operation of the space-time turbo coding apparatus configured as described above will be described below. In addition, it is assumed in following descriptions that modulation scheme instructing section 500 instructs to apply QPSK modulation to the systematic bits and 16QAM to the parity bits.

[0093]

First, as in Embodiment 1, turbo coding section 100 performs turbo coding on transmission data. As a result of turbo coding, obtained systematic bits are output to mapping section 300-1, and parity bits are output to mapping section 300-2 from selecting section 200.

[0094]

Meanwhile, when the number-of-retransmission-time information is input to retransmission control section 800, retransmission control section 800 detects the number of retransmission times, and determines whether or not to operate bit arrangement determining section 600 and arrangement axis exchanging section 700.

[0095]

Herein, it is assumed that retransmission does not occur (the number of retransmission times is zero) for further descriptions.

[0096]

When the number of retransmission times is zero, by bit arrangement determining section 600 being controlled by retransmission control section 800, the

systematic bits and parity bits are both arranged on the in-phase axis and quadrature axis, and second parity bits output from coding sections 120-1 and 120-2 are exchanged in the bit arrangement (see FIG. 7A). Meanwhile, arrangement axis exchanging section 700 does not operate. In other words, when the number of retransmission times is zero, the same bit arrangement as in above-mentioned Embodiment 3 is adopted.

[0097]

When the bit arrangements are thus determined, the determined bit arrangements are notified to corresponding mapping sections 300-1 and 300-2. Then, mapping section 300-2 modulates the systematic bits to perform symbol mapping. Also, mapping section 300-2 modulates the parity bits to perform symbol mapping.

[0098]

Thus modulated and symbol-mapped systematic bits and parity bits are subjected to the predetermined radio transmission processing (such as D/A conversion and upconverting) in radio transmission sections 400-1 and 400-2, and transmitted from antennas 450-1 and 450-2, respectively. Respective signals transmitted from antennas 450-1 and 450-2 are spatially multiplexed and received in the reception apparatus.

[0099]

At this time, when a signal received in the reception apparatus includes many errors and data of required quality is not obtained, the reception apparatus issues a retransmission request. The retransmission request is received in a receiving section not shown in the figure, and the number of times the retransmission request is

received is input to retransmission control section 800 as the number-of-retransmission-time information.

[0100]

In this way, retransmission control section 800 detects that the number of retransmission times is one.

[0101]

Further, when the retransmission request is received in the receiving section not shown in the figure, the transmission data required to be retransmitted is turbo coded again in turbo coding section 100. Then, as described above, obtained systematic bits are output to mapping section 300-1, and parity bits are output to mapping section 300-2 from selecting section 200.

[0102]

When the number of retransmission times is one, by bit arrangement determining section 600 being controlled by retransmission control section 800, the systematic bits and parity bits are both arranged on the in-phase axis and quadrature axis, and first parity bits output from coding sections 120-1 and 120-2 are exchanged in the bit arrangement (see FIG. 7B). Meanwhile, arrangement axis exchanging section 700 does not operate.

[0103]

Thus, when the number of retransmission times is one, by changing the bit arrangement from the case that the number of retransmission times is zero, the same error is prevented from occurring on the propagation path.

[0104]

Herein, it is further assumed to continue the explanation that the signal received in the reception apparatus still has many errors and data of required quality is not obtained. In other words, a second retransmission request is issued from the reception

apparatus, and the number-of-retransmission-time information is input to retransmission control section 800.

[0105]

Thus retransmission control section 800 detects that the number of retransmission times is two.

[0106]

Then, the transmission data required to be retransmitted is turbo coded again in turbo coding section 100. Then, as described above, obtained systematic bits are output to mapping section 300-1, and parity bits are output to mapping section 300-2 from selecting section 200.

[0107]

When the number of retransmission times is two, by bit arrangement determining section 600 being controlled by retransmission control section 800, the systematic bits and parity bits are both arranged on the in-phase axis and quadrature axis, and second parity bits output from coding sections 120-1 and 120-2 are exchanged in the bit arrangement. Further, arrangement axis exchanging section 700 exchanges the arrangement axes of coordinates to place the systematic bits and parity bits (see FIG. 7C).

[0108]

Thus, when the number of retransmission times is two, instead of changing the bit arrangement, the arrangement axes are exchanged on coordinates for bit arrangement to prevent the same error from occurring on the propagation path.

[0109]

In this way, according to this Embodiment, the systematic bits and parity bits are modulated in mutually different modulation schemes, while changing the bit

arrangement whenever retransmission is requested, and the systematic bits and parity bits are transmitted from respective different antennas. Thus it is possible to suppress the probability of occurrence of degeneracy in the reception apparatus, and prevent deterioration in demodulation performance without increasing interfering power. Further, when the fading variation is slow, it is possible to prevent the same error from occurring in retransmission, and improve the throughput of the entire system.

[0110]

In addition, in this Embodiment, arrangement axes are exchanged when the number of retransmission times is two, but in regard to a combination of a change in bit arrangement and exchange of arrangement axes, the combination is capable of being carried into practice with various modifications thereof.

[0111]

Further, described in each of the above-mentioned Embodiments are a combination of BPSK modulation and QPSK modulation and another combination of QPSK modulation and 16QAM as examples. However, the invention is not limited to such examples, and applicable to any combinations of modulation schemes that are different from each other, such as a combination of 16QAM and 64QAM and another combination of BPSK modulation and 16QAM.

[0112]

Furthermore, by way of explanation the number of antennas is two in each of the above-mentioned Embodiments, but the invention is not limited to such a number, and applicable to any number of antennas.

[0113]

[Effects of the Invention]

As described above, according to the present invention, it is possible to prevent demodulation

performance from deteriorating without increasing interfering power.

[BRIEF DESCRIPTION OF DRAWINGS]

[FIG.1]

A block diagram showing a principal configuration of a space-time turbo coding apparatus according to Embodiment 1 of the invention;

[FIGS.2]

Diagrams each showing an example of signal point constellations according to Embodiment 1;

[FIG.3]

A block diagram showing a principal configuration of a space-time turbo coding apparatus according to Embodiment 2 of the invention;

[FIGS.4]

Diagrams each showing an example of bit arrangements according to Embodiment 2;

[FIG.5]

A diagram showing an example of bit arrangements according to Embodiment 3 of the invention;

[FIG.6]

A block diagram showing a principal configuration of a space-time turbo coding apparatus according to Embodiment 4 of the invention;

[FIGS.7]

Diagrams each showing an example (first transmitted signal) of bit arrangements according to Embodiment 4;

[FIGS.8]

Diagrams each showing an example of signal point constellations in applying BPSK modulation in space-time turbo coding.

[Description of the Symbols]

100: TURBO CODING SECTION

110: INTERLEAVER

120-1, 120-2: CODING SECTION

130-1, 130-2: PUNCTURING SECTION



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140: DEINTERLEAVER

200: SELECTING SECTION

300-1, 300-2: MAPPING SECTION

400-1, 400-2: RADIO TRANSMISSION SECTION

500: MODULATION SCHEME INSTRUCTING SECTION

600: BIT ARRANGEMENT DETERMINING SECTION

700: ARRANGEMENT AXIS EXCHANGING SECTION

800: RETRANSMISSION CONTROL SECTION

[NAME OF DOCUMENT] ABSTRACT

[Abstract]

[Object] To prevent deterioration in demodulation performance without increasing interfering power.

[Overcoming Means] A turbo coding section 100 performs turbo coding on transmission data, and outputs systematic bits and parity bits. A selecting section 200 selects either of two parity bit sequences output from the turbo coding section 100 to output to a mapping section 300-2. Another mapping section 300-1 modulates the systematic bits and performs symbol mapping. The mapping section 300-2 modulates the parity bits and performs symbol mapping. At this time, mapping sections 300-1 and 300-2 perform modulation in modulation schemes instructed from a modulation scheme instructing section 500. The modulation scheme instructed to the mapping section 300-2 is different from the modulation scheme instructed to another mapping section 300-1.

[Selected Drawing] FIG.1

FIG.1

100 TURBO CODING SECTION  
TRANSMISSION DATA  
110 INTERLEAVER  
120-1 120-2 CODING SECTION  
130-1 130-2 PUNCTURING SECTION  
140 DEINTERLEAVER  
200 SELECTING SECTION  
300-1 300-2 MAPPING SECTION  
400-1 400-2 RADIO TRANSMISSION SECTION  
500 MODULATION SCHEME INSTRUCTING SECTION

FIG.3

100 TURBO CODING SECTION  
TRANSMISSION DATA  
110 INTERLEAVER  
120-1 120-2 CODING SECTION  
130-1 130-2 PUNCTURING SECTION  
140 DEINTERLEAVER  
200 SELECTING SECTION  
300-1 300-2 MAPPING SECTION  
400-1 400-2 RADIO TRANSMISSION SECTION  
500 MODULATION SCHEME INSTRUCTING SECTION  
600 BIT ARRANGEMENT DETERMINING SECTION

FIG.6

100 TURBO CODING SECTION  
TRANSMISSION DATA  
110 INTERLEAVER  
120-1 120-2 CODING SECTION  
130-1 130-2 PUNCTURING SECTION  
140 DEINTERLEAVER  
200 SELECTING SECTION  
300-1 300-2 MAPPING SECTION  
400-1 400-2 RADIO TRANSMISSION SECTION  
500 MODULATION SCHEME INSTRUCTING SECTION

600 BIT ARRANGEMENT DETERMINING SECTION  
700 ARRANGEMENT AXIS EXCHANGING SECTION  
800 RETRANSMISSION CONTROL SECTION  
NUMBER-OF-RETRANSMISSION-TIME INFORMATION

FIG.7

(A) INITIAL TRANSMISSION SIGNAL  
(B) TRANSMISSION SIGNAL AT RETRANSMISSION TIME (SECOND  
TIME)  
(C) TRANSMISSION SIGNAL AT RETRANSMISSION TIME (THIRD  
TIME)